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In electronics, emitter-coupled logic (ECL) is a high-speed integrated circuit bipolar transistor logic family. ECL uses an overdriven BJT differential amplifier with single-ended input and limited emitter current to avoid the saturated (fully on) region of operation and its slow turn-off behavior. As the current is steered between two legs of an emitter-coupled pair, ECL is sometimes called ...

Emitter-coupled logic - Wikipedia

This page contains the complete set of materials for my FPGA & Verilog design course which I taught in Isfahan University of Technology, 2010.

FPGA & Verilog Design “ Mohammad S. Sadri - Googoolia

The Vision of the Department of Electronics and Communication Engineering, National Institute of Technology Silchar is to be a model of excellence for undergraduate and post graduate education and research in the country.

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August 20, 2018 Fairport, New York (August 20, 2018) Vanteon Corporation, an electronic systems design and engineering company, is pleased to announce the latest change within the sales organization, with the promotion of Alan Diamond to the newly created position of Director of Sales and Marketing.

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An overview of through-silicon-via technology and

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10 BEST Semiconductor Equipment Supplier Rankings for 2016

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Solder - Wikipedia

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Electronic Devices: Conventional Current Version â€“ Thomas

Microelectromechanical systems (MEMS) are a fast-growing field in microelectronics. MEMS are commonly used as actuators and sensors with a wide variety of applications in health care, automotives, and the military. The MEMS production cycle can be classified as three basic steps: (1) design process, (2) manufacturing process, and (3) operating cycle.

Journal of Quality and Reliability Engineering - Hindawi

â„©: ELECTRIC POWER SUPPLY SYSTEM COOPERATED BY A BATTERY AND A SUPER CAPACITOR (2016/04/28 US9,543,784 B2) Shyh-Jier Huang, Mi-Ching Tsai, Ying-Rongji Chen

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